

### **Specification Amendments**

Please amend the specification, as indicated in the following paragraph:

Please replace the paragraph on page 15, lines 24 – 29:

The boot-capable devices 210A and 210B may be either single-instance I/O devices 108 or multi-instance I/O devices 110 (see Figure 1). Interrupts 226A coming from any one of the boot-capable devices 210A are received into the OR gate 222A, to produce a single output signal 224A. Likewise, interrupts 226B coming from any one of the boot-capable devices ~~[[218B]] 210B~~ are received into the OR gate 222B, to produce a single output signal 224B.

Please replace the paragraph on page 20, lines 21 – 26:

Using domain one 118B as an example, when a hard reset is invoked, such as by pushing a front panel button, the reset handler 182B issues the signal 196B. The signal 196B is one of two inputs into the multiplexer 192B. The other input to the multiplexer 192B is the hard reset signal 184A, from domain zero 118A. The hard reset signal 184A is an input for each of the N-1 multiplexers 192.